

APPROVED	O.G. FIG.	
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Title: Apparatus and Methods of Semiconductor
 Wafer Processing Equipment
 Inventor: Soucy et al.
 Serial No. 09/897,820
 Atty Docket No. APE-001
 Atty: John V. Forcier

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FIG. 1D
 PRIOR ART

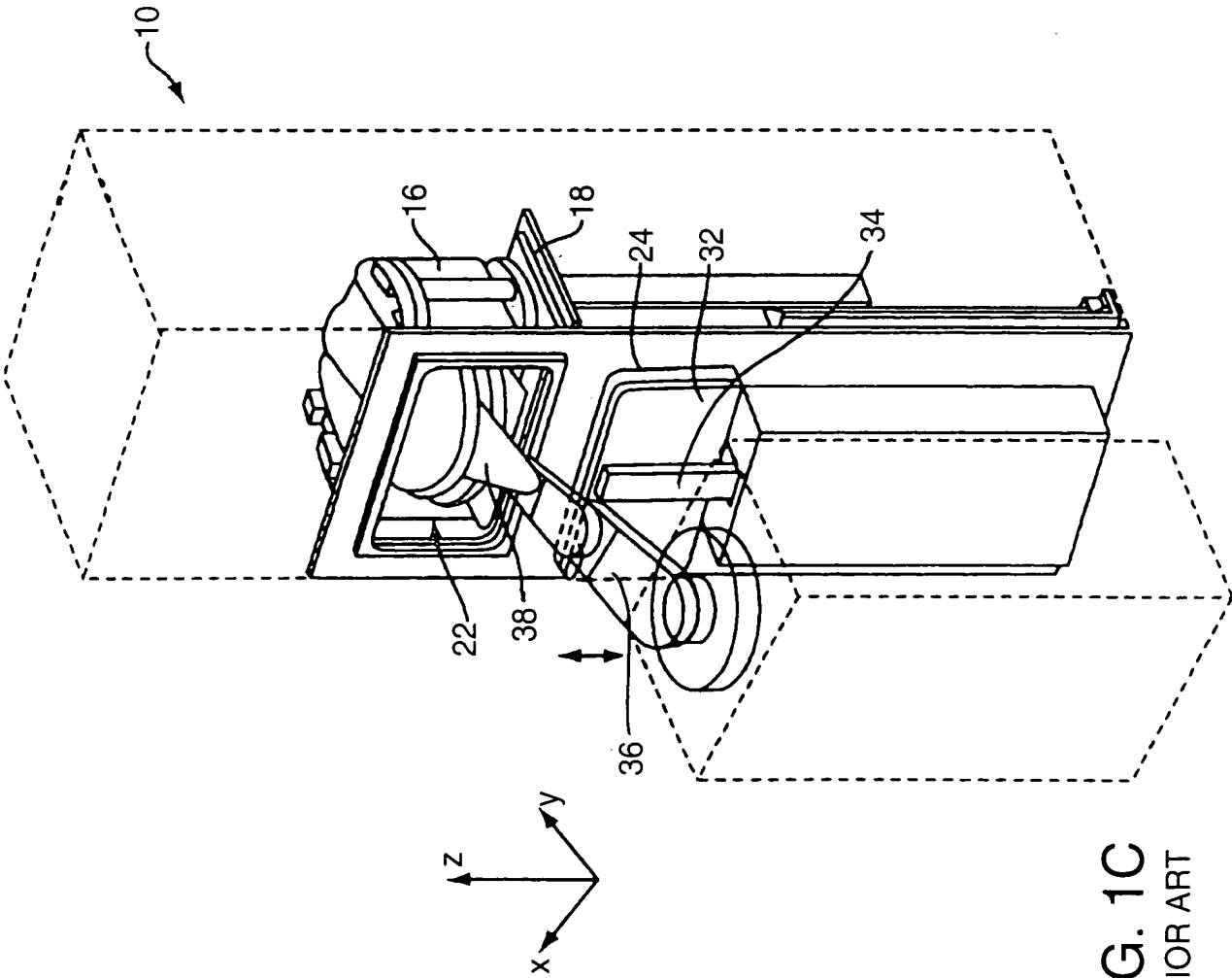
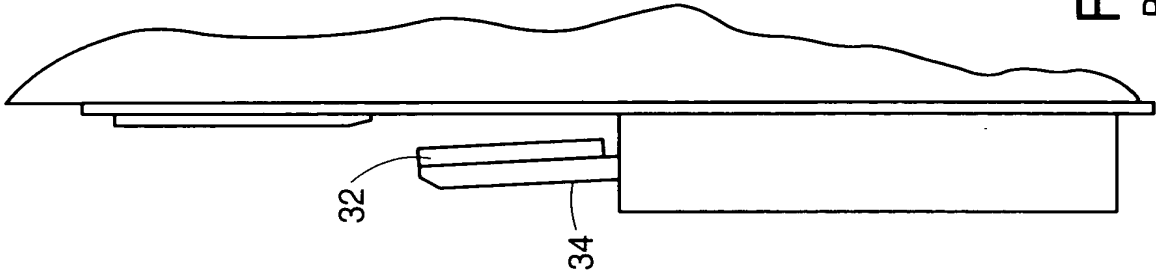


FIG. 1C
 PRIOR ART

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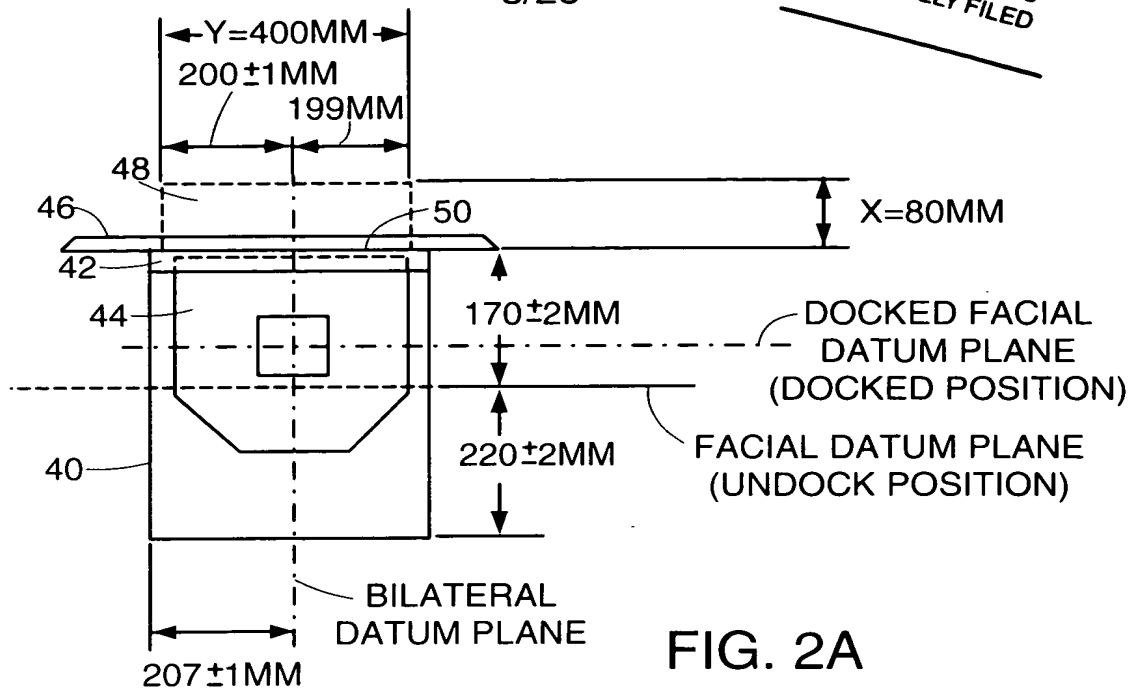


FIG. 2A

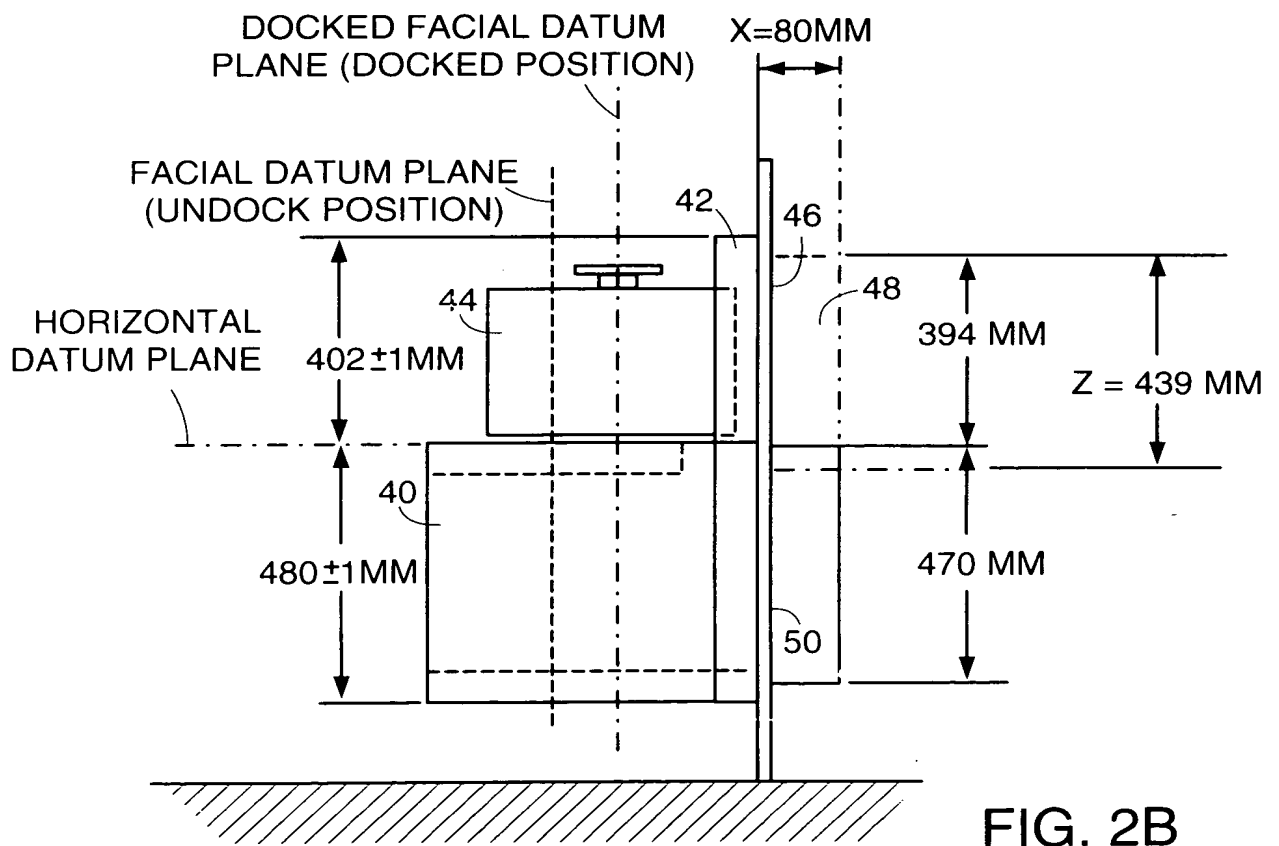


FIG. 2B

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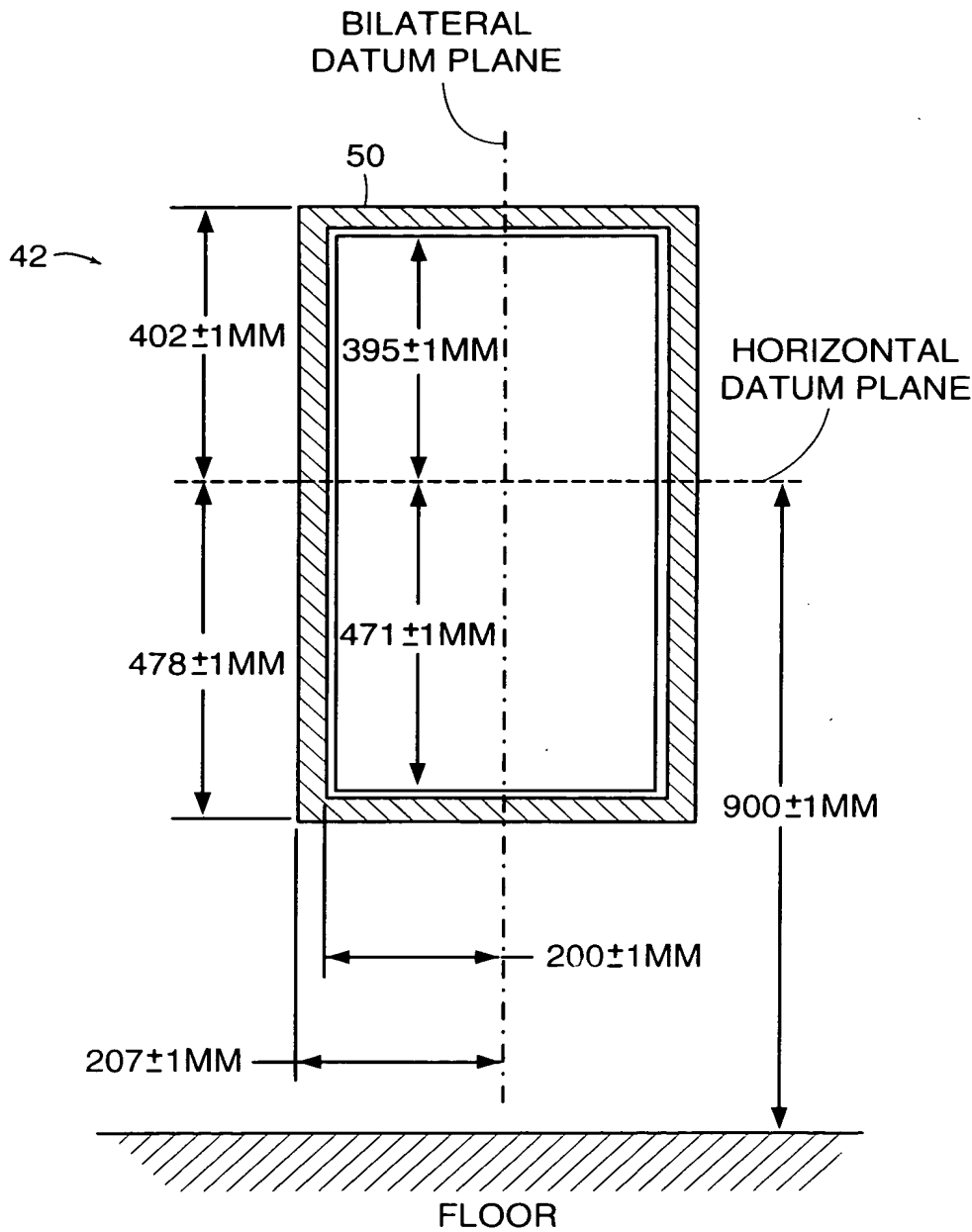


FIG. 2C

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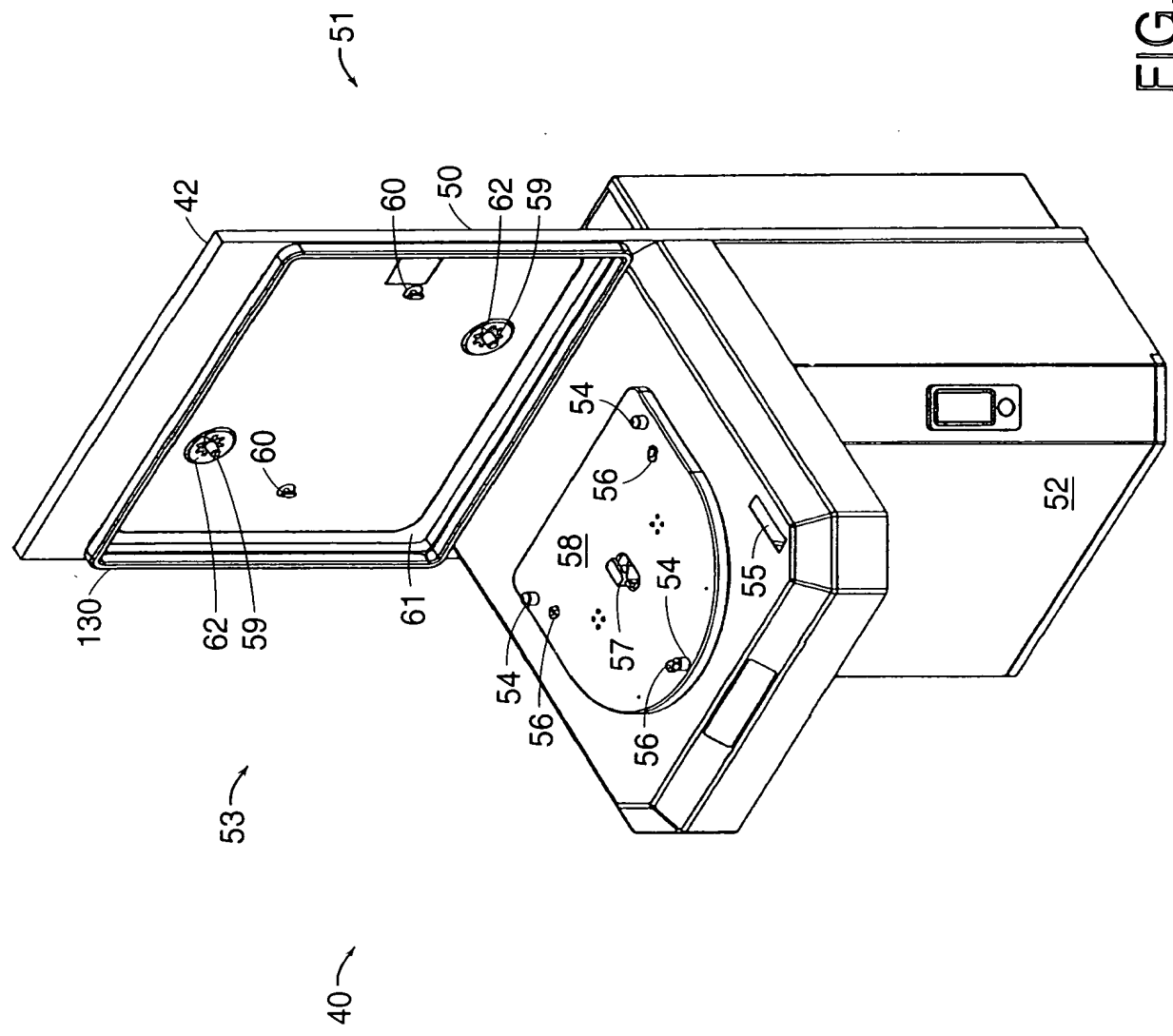


FIG. 3

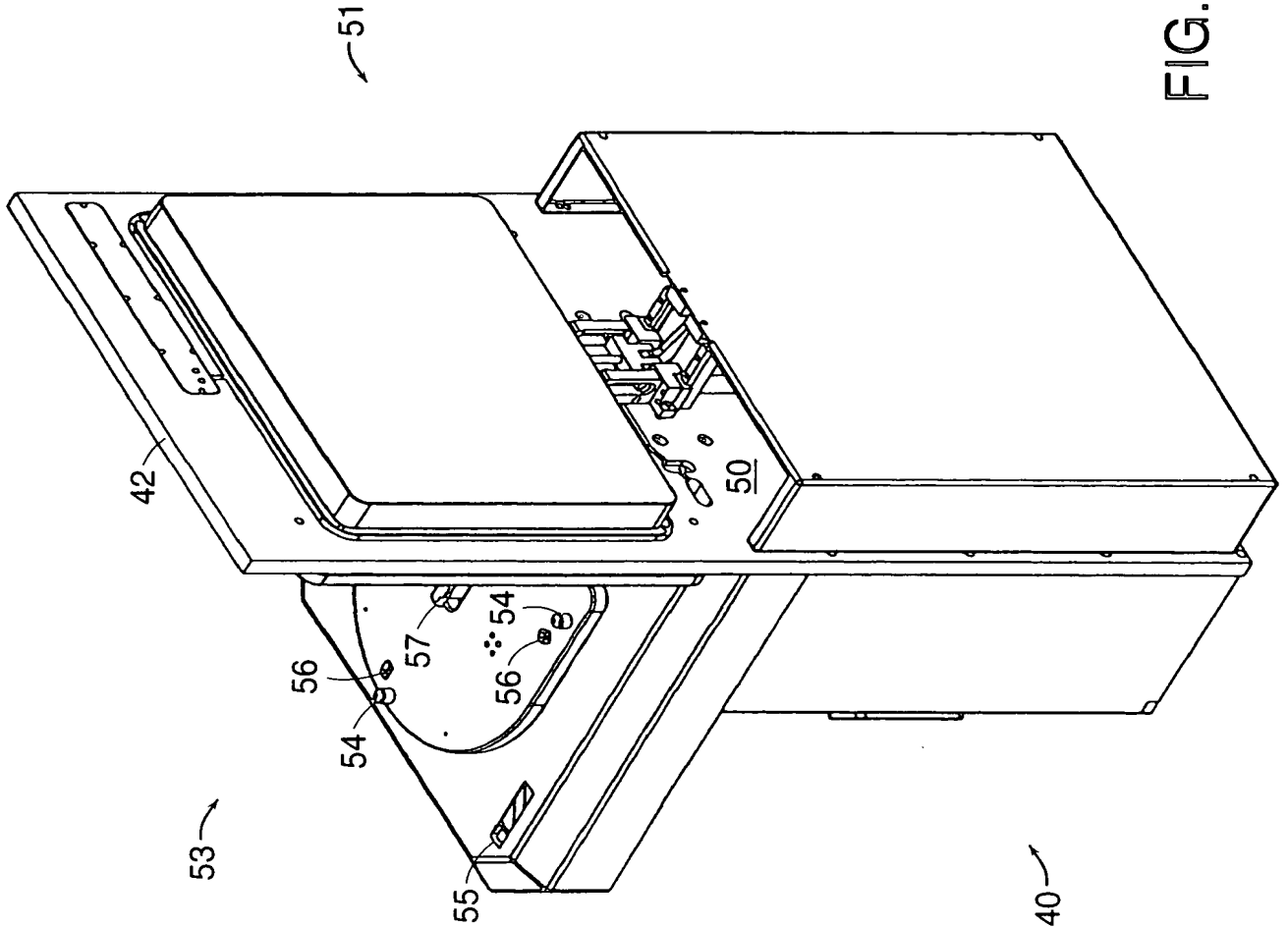
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FIG. 4



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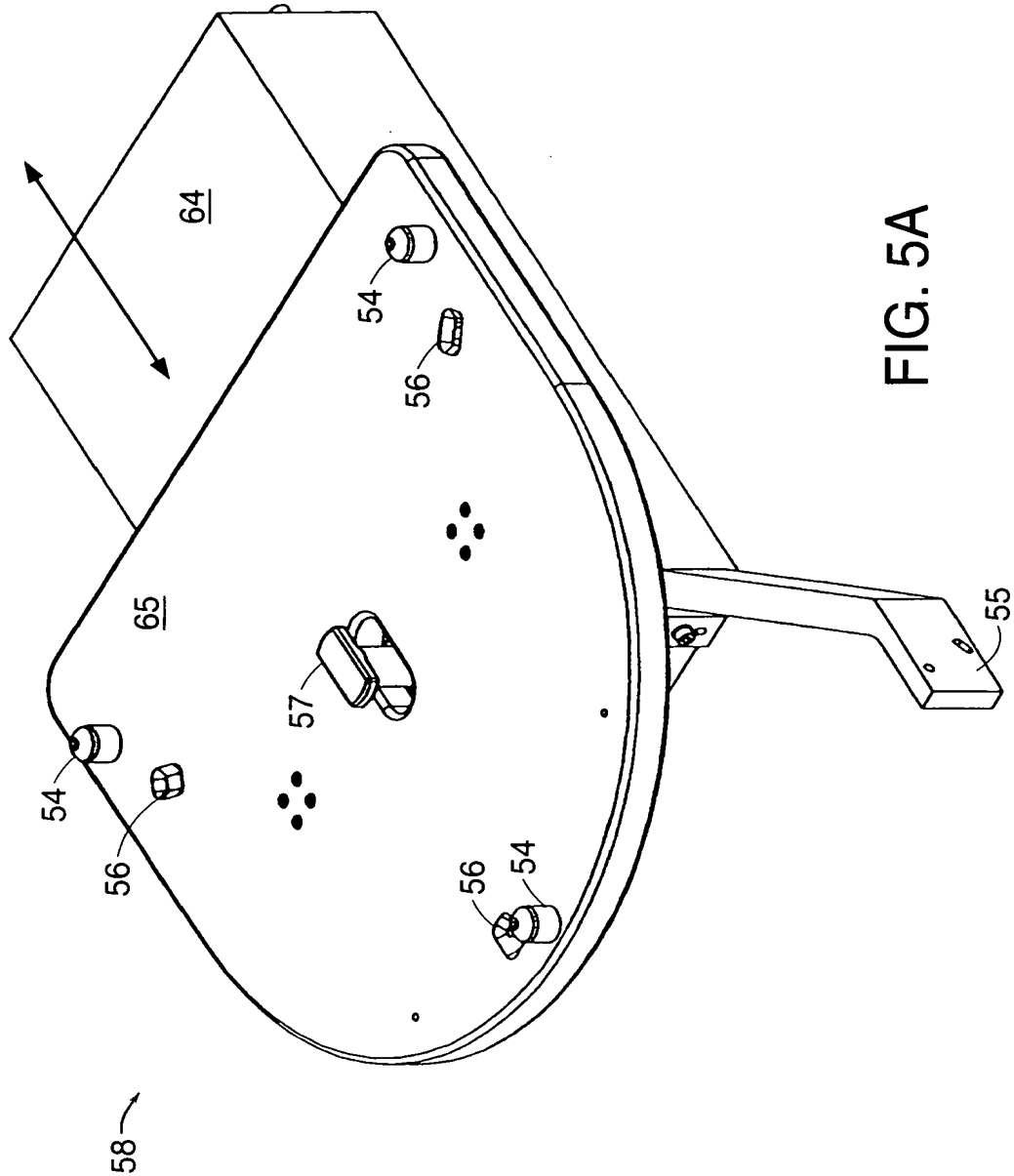


FIG. 5A

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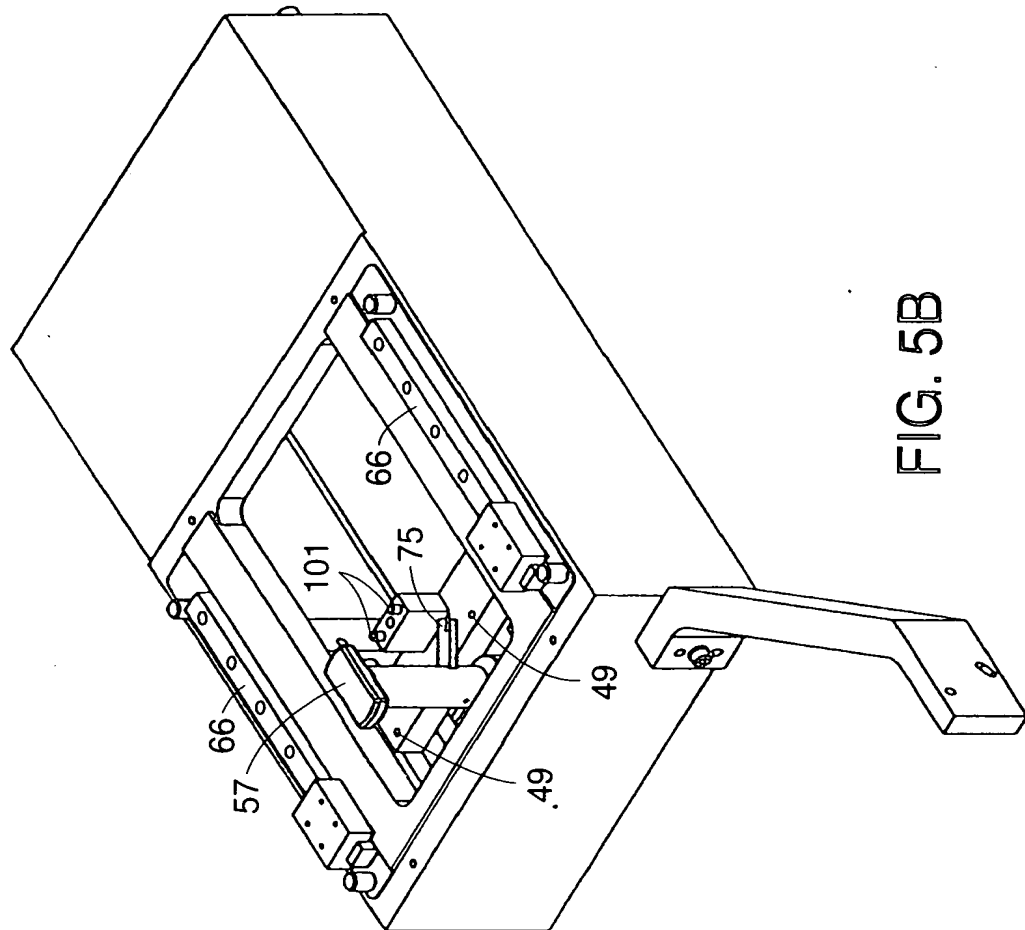


FIG. 5B

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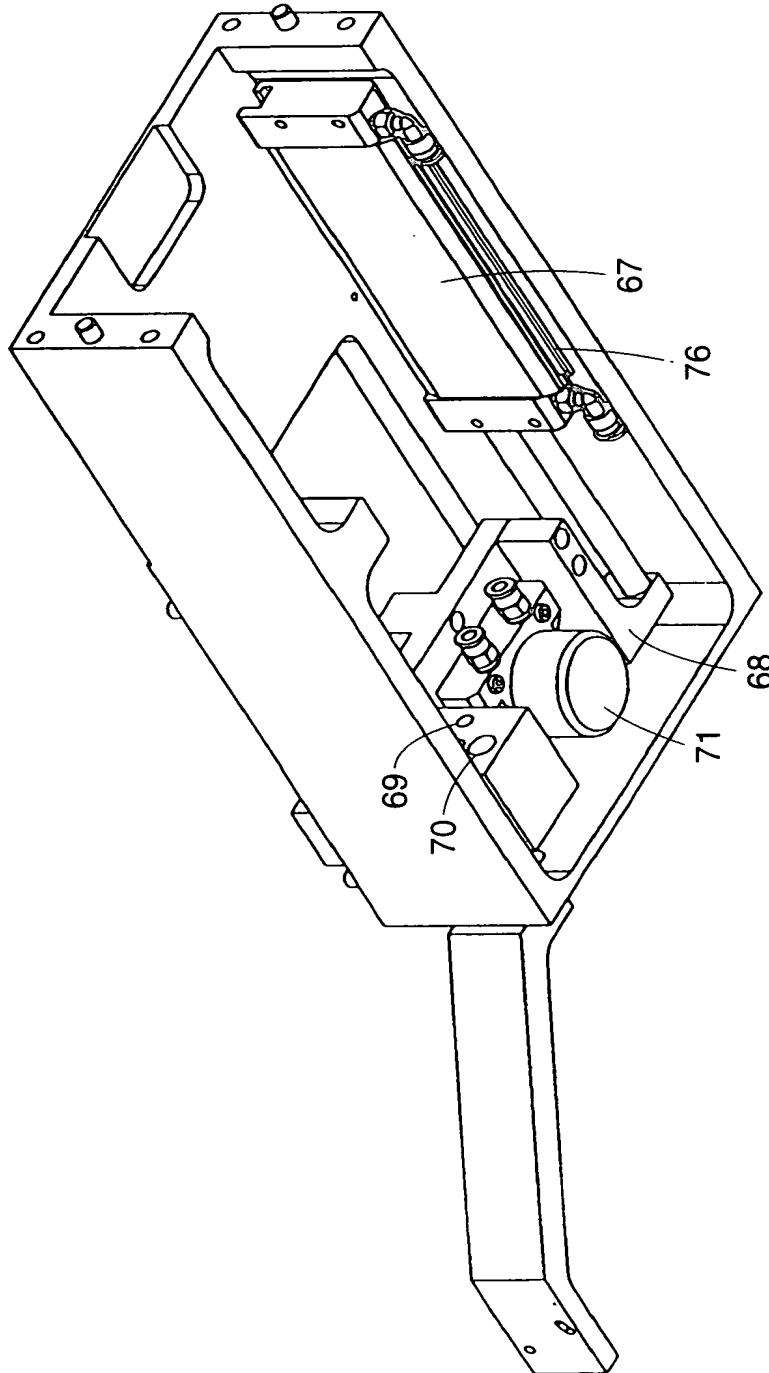


FIG. 5C

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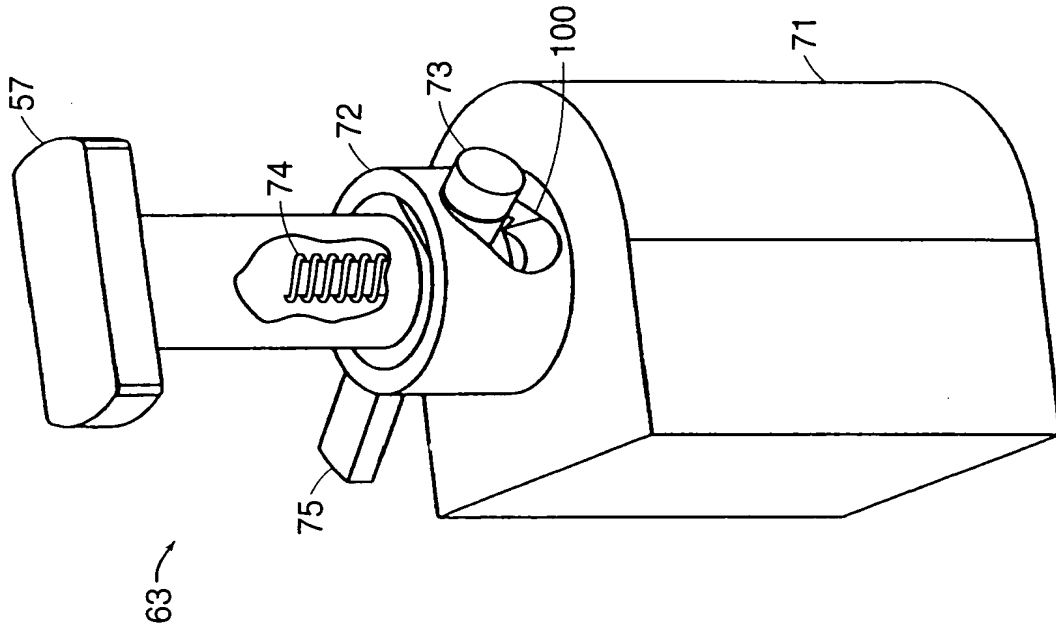


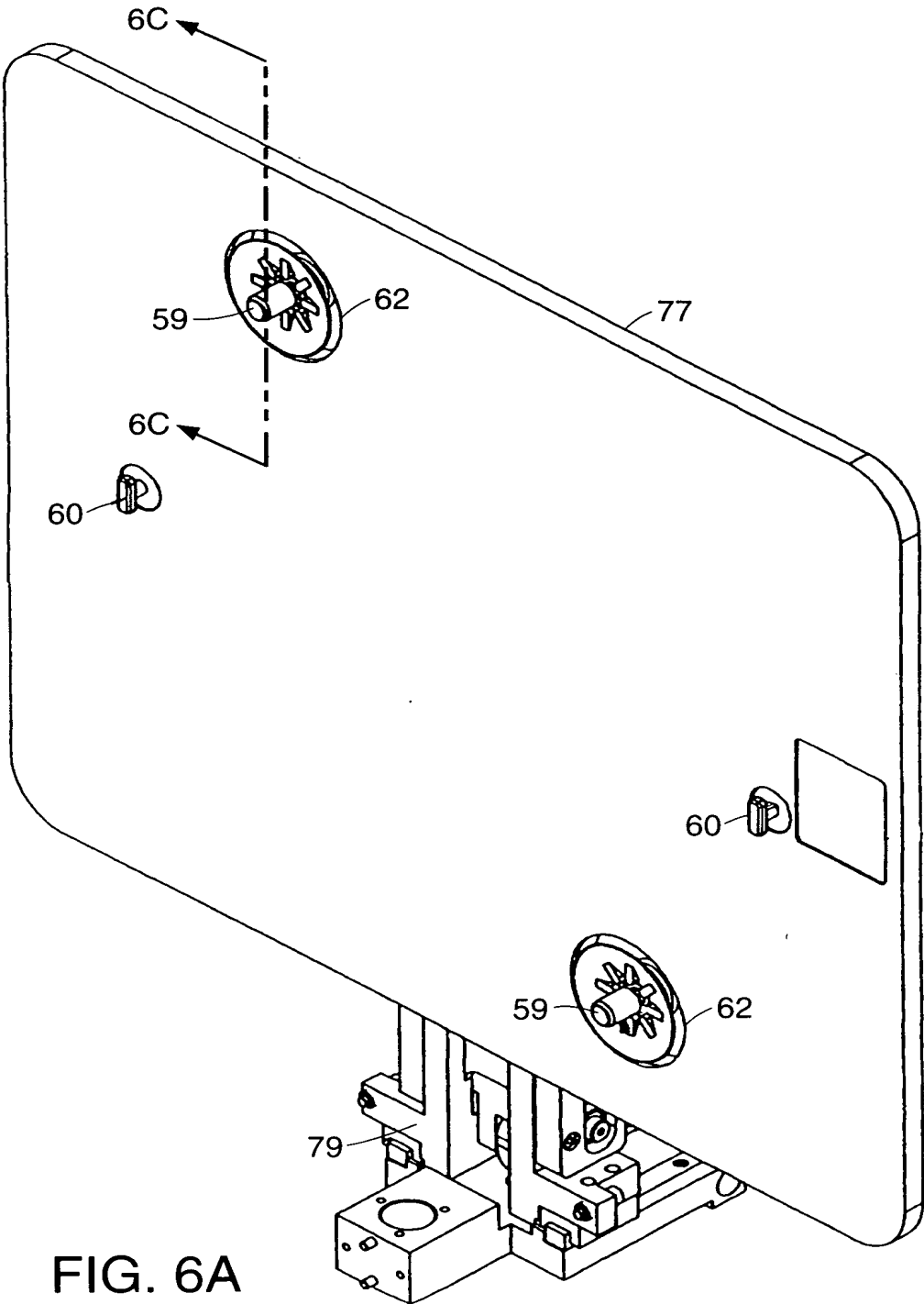
FIG. 5D

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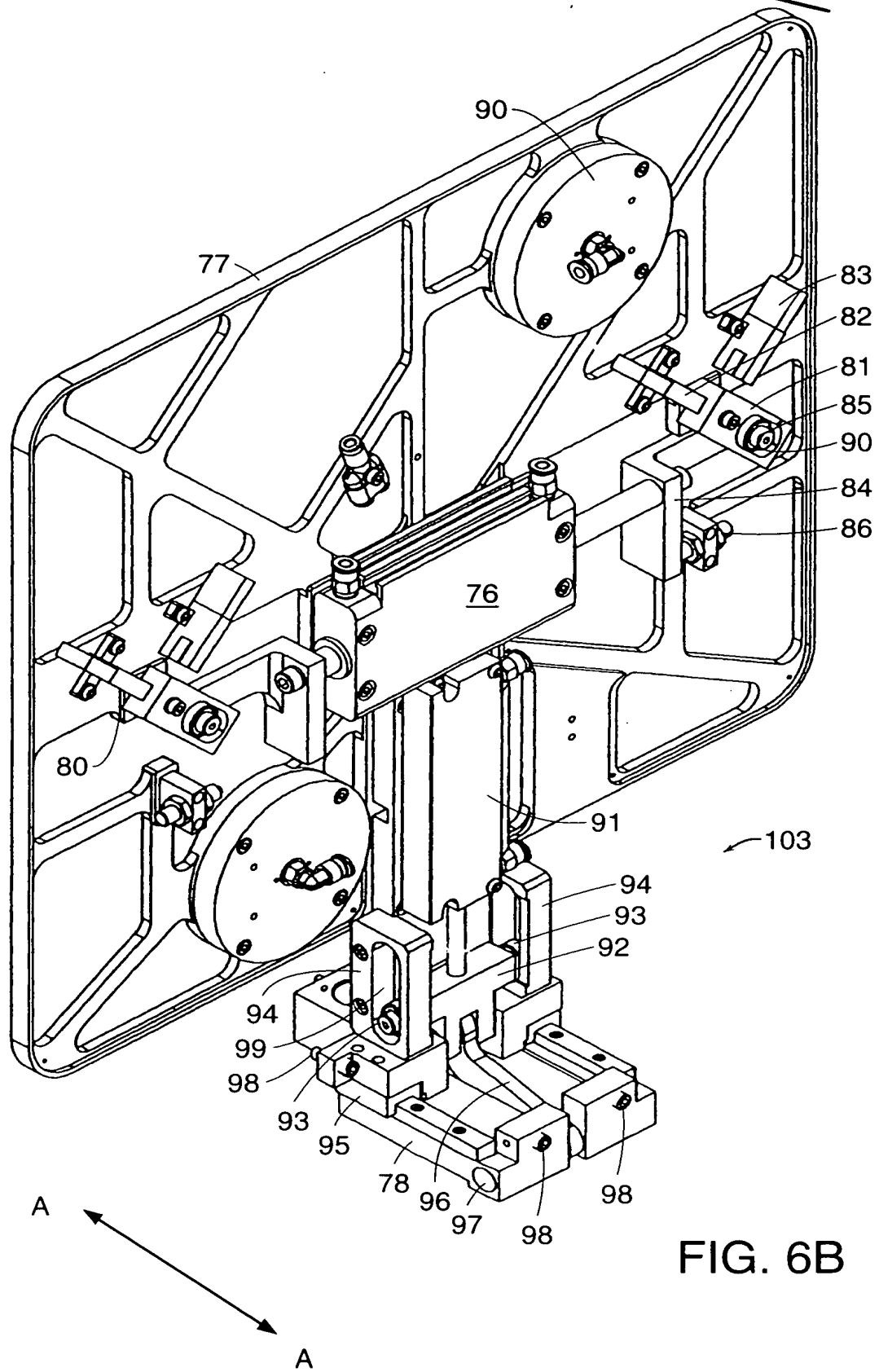


FIG. 6B

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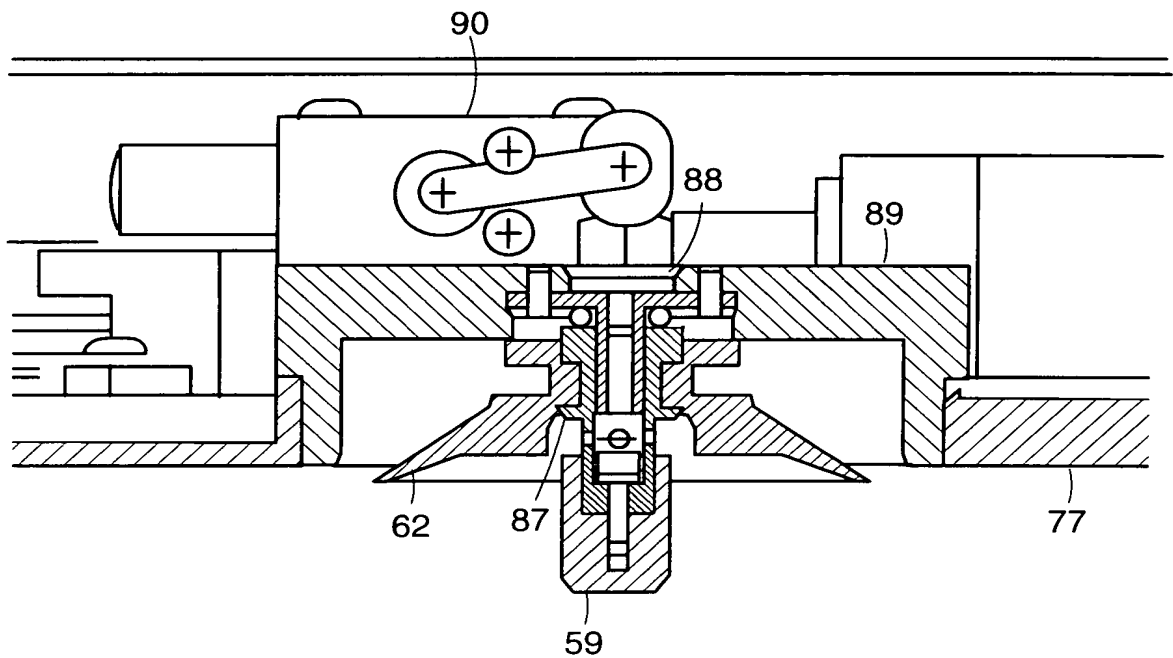


FIG. 6C

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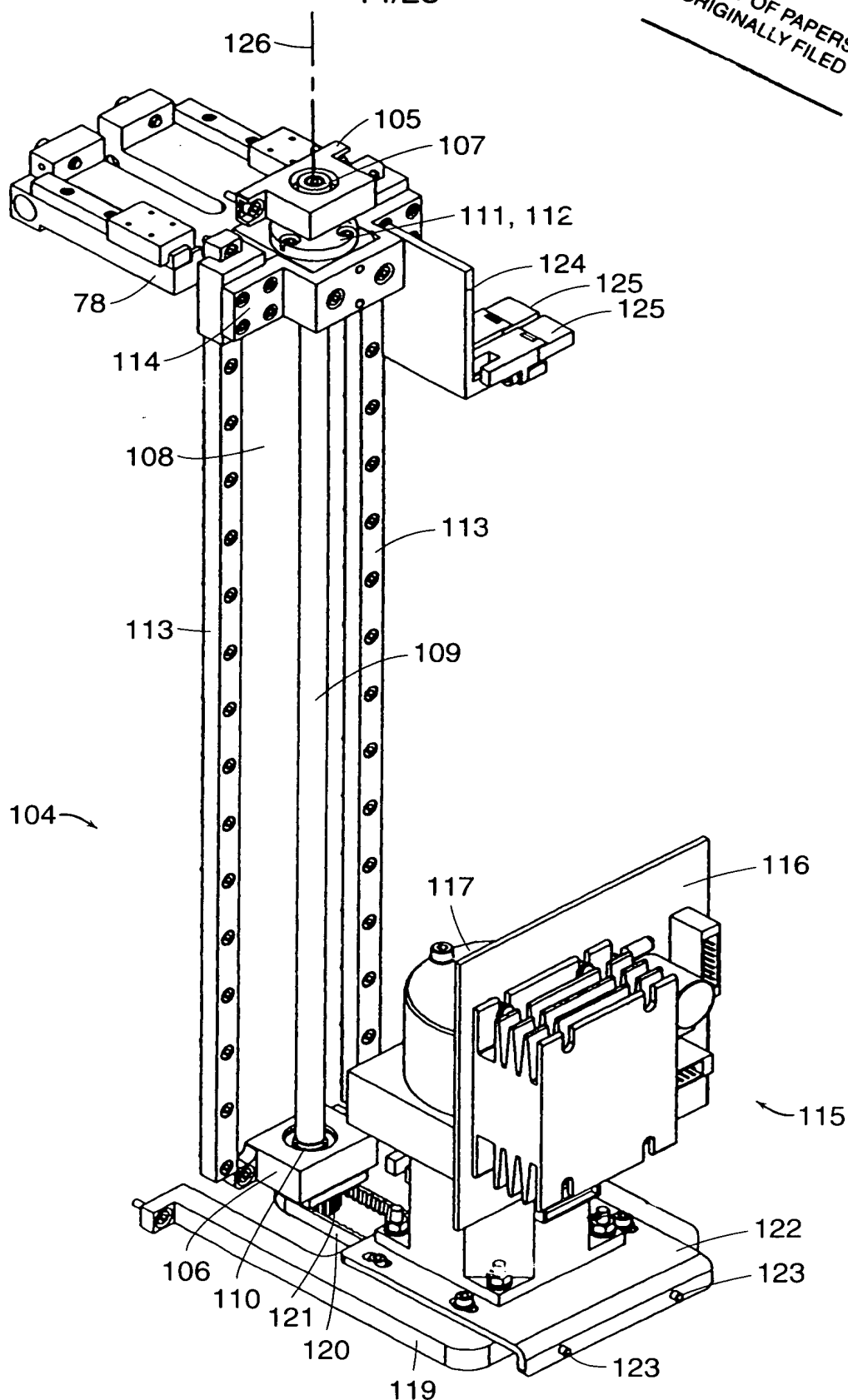


FIG. 7

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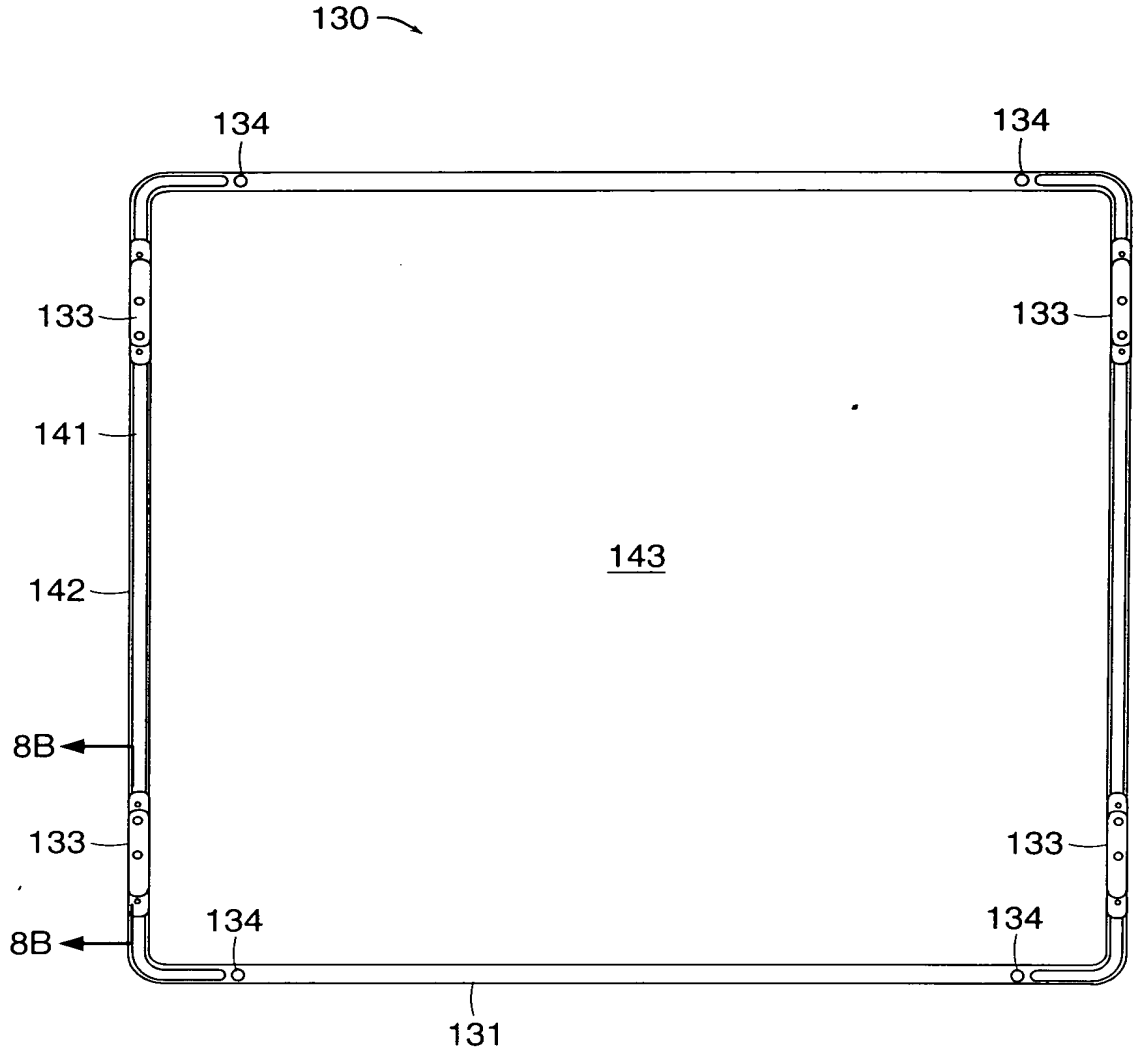


FIG. 8A

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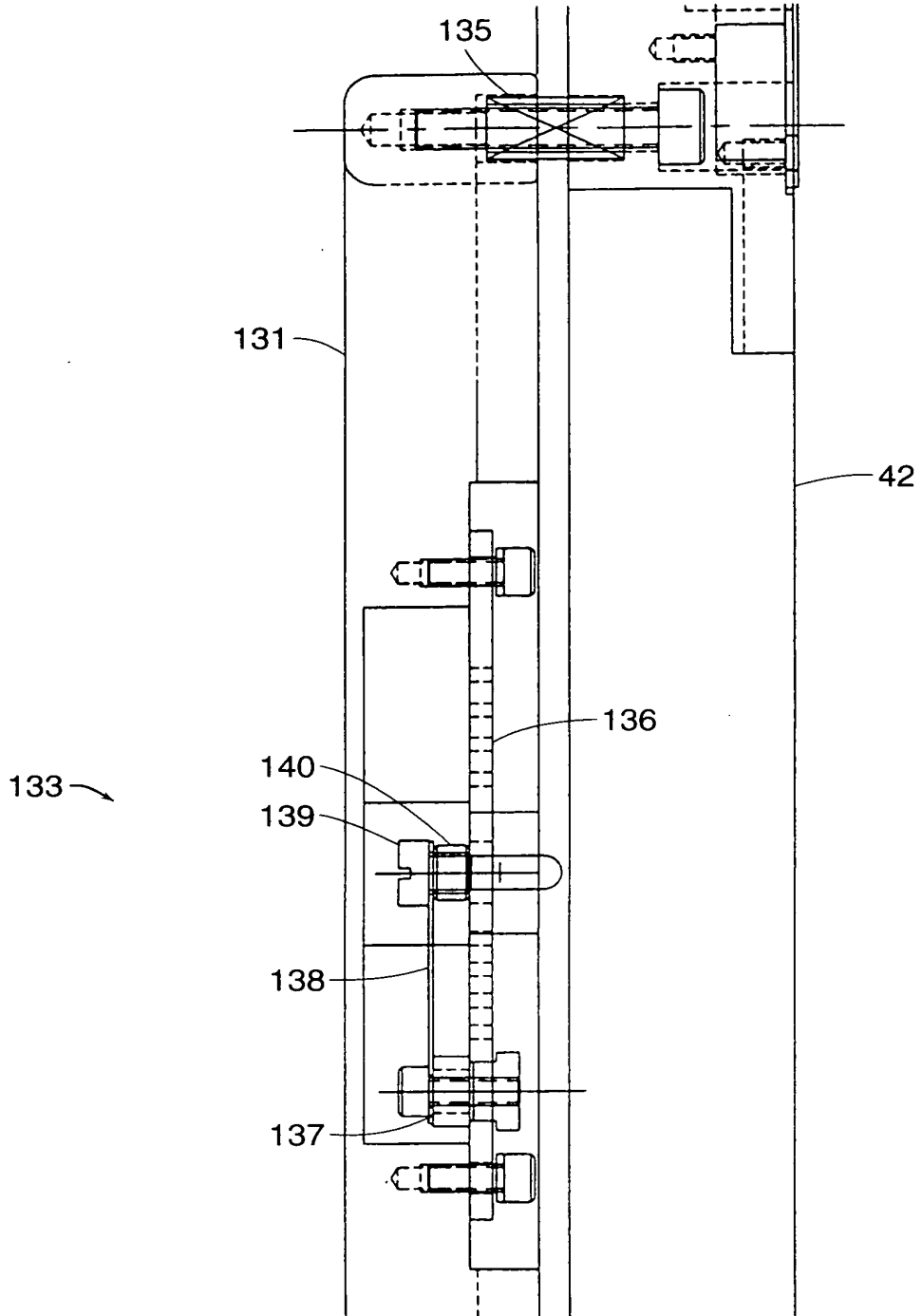


FIG. 8B

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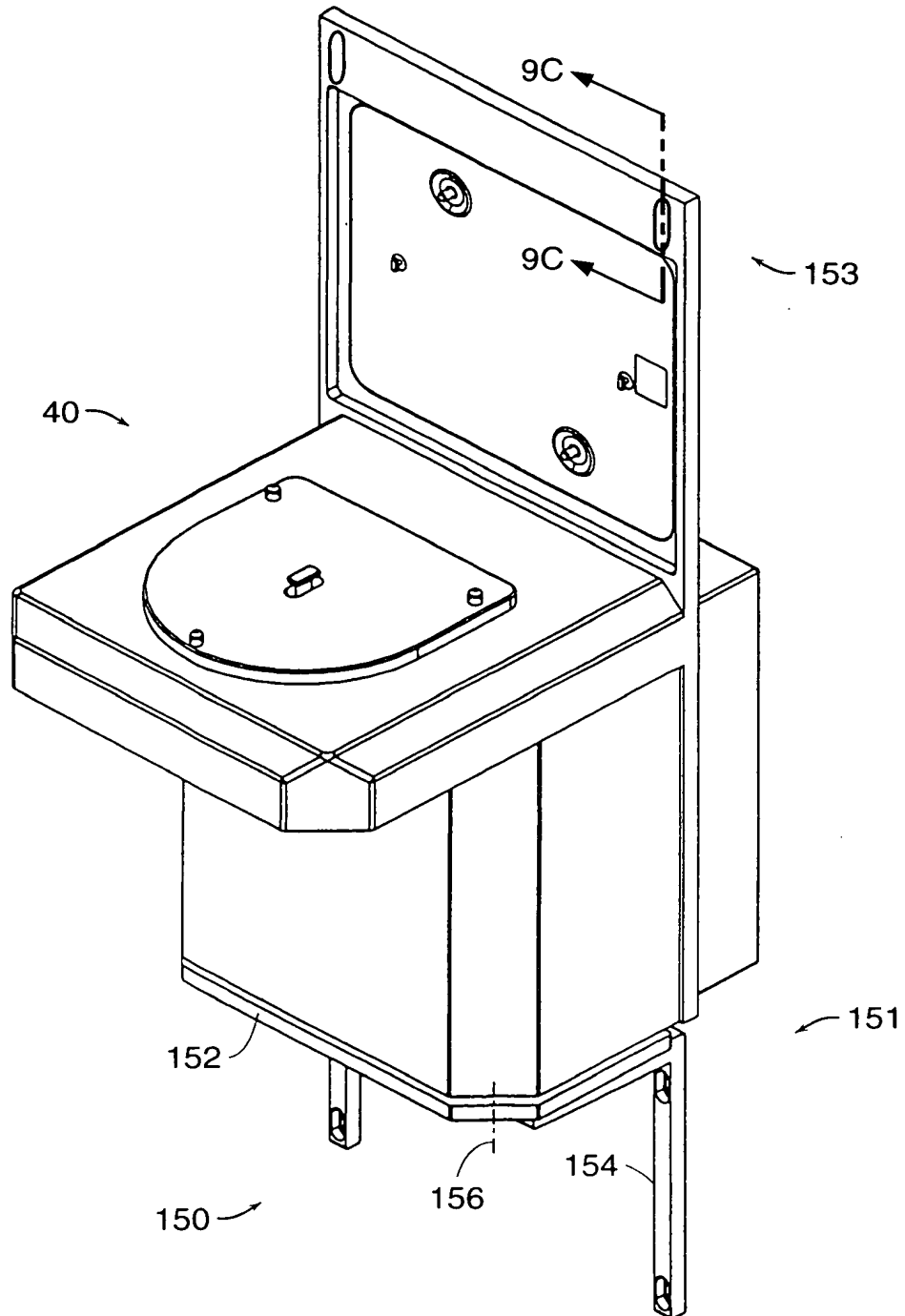


FIG. 9A

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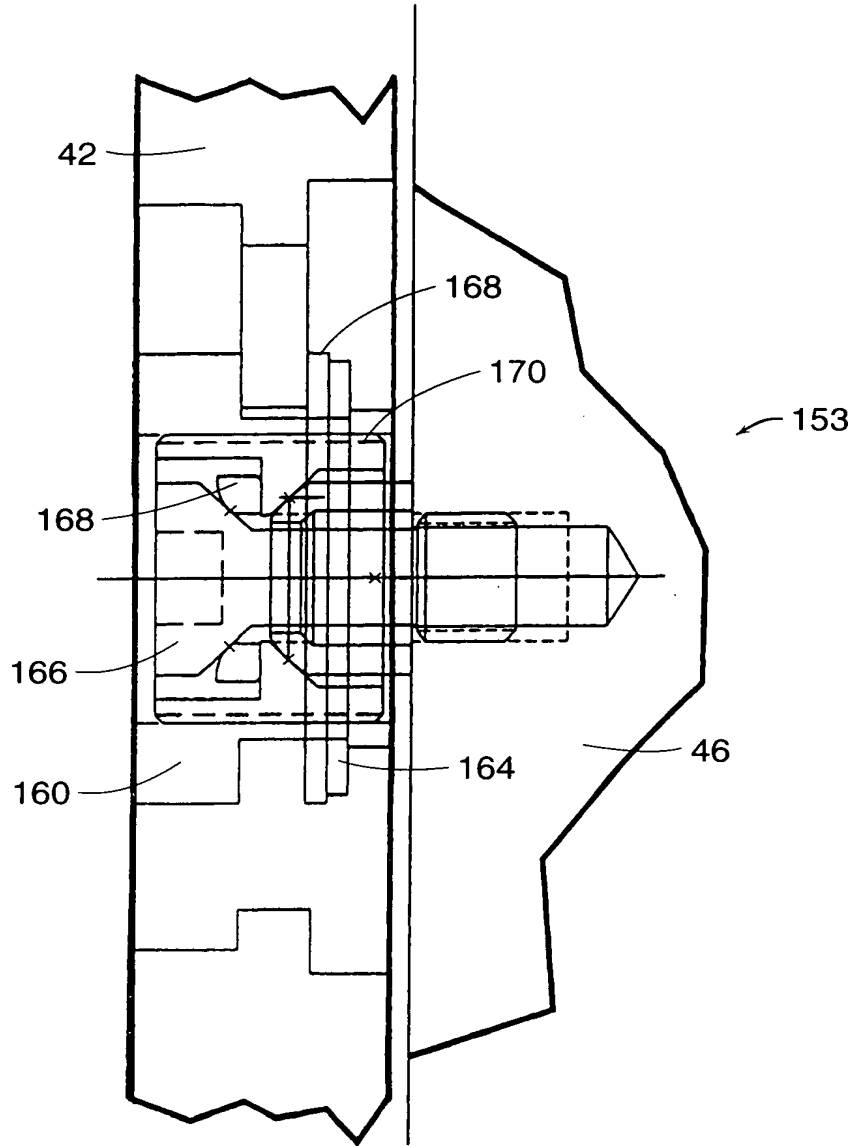


FIG. 9C

STATUS DISPLAY

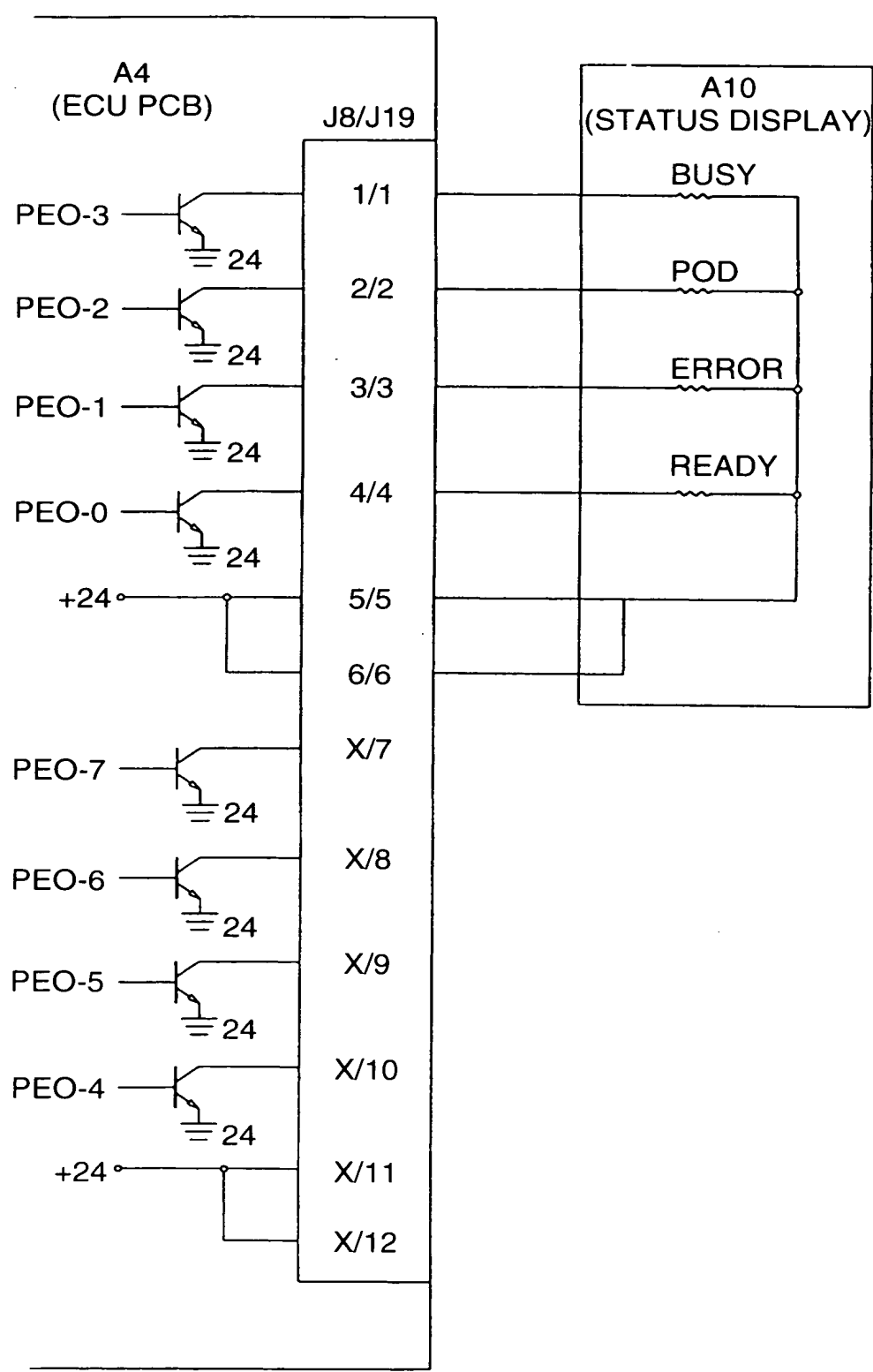


FIG. 10A

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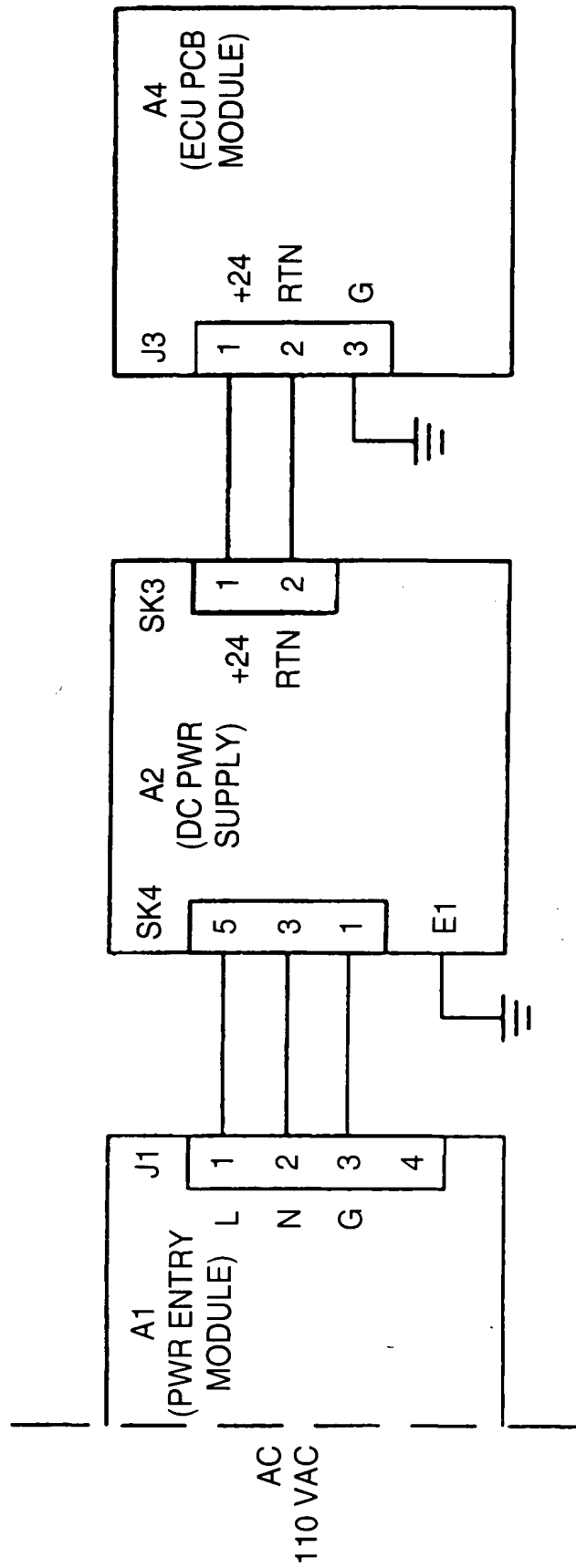


FIG. 10B

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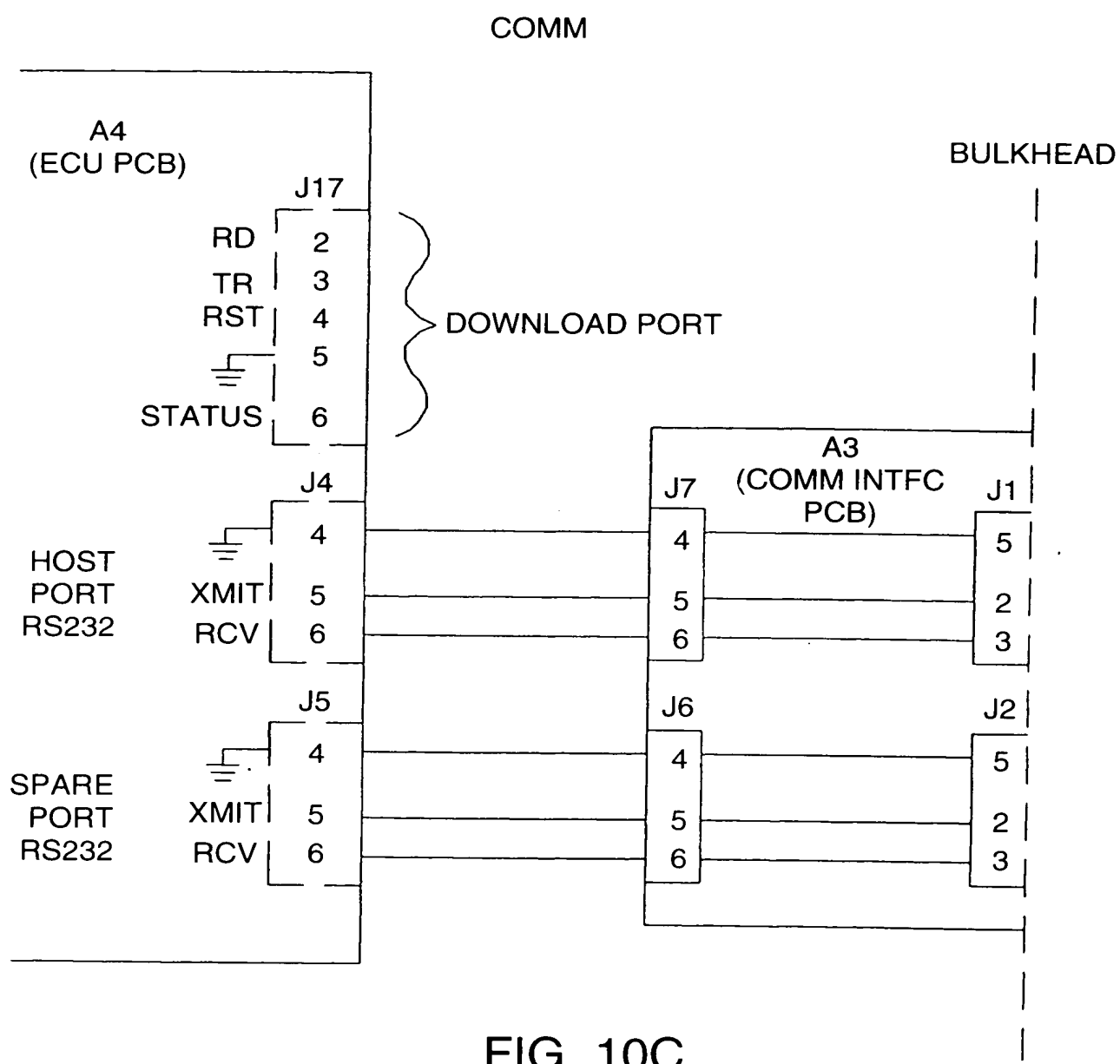


FIG. 10C

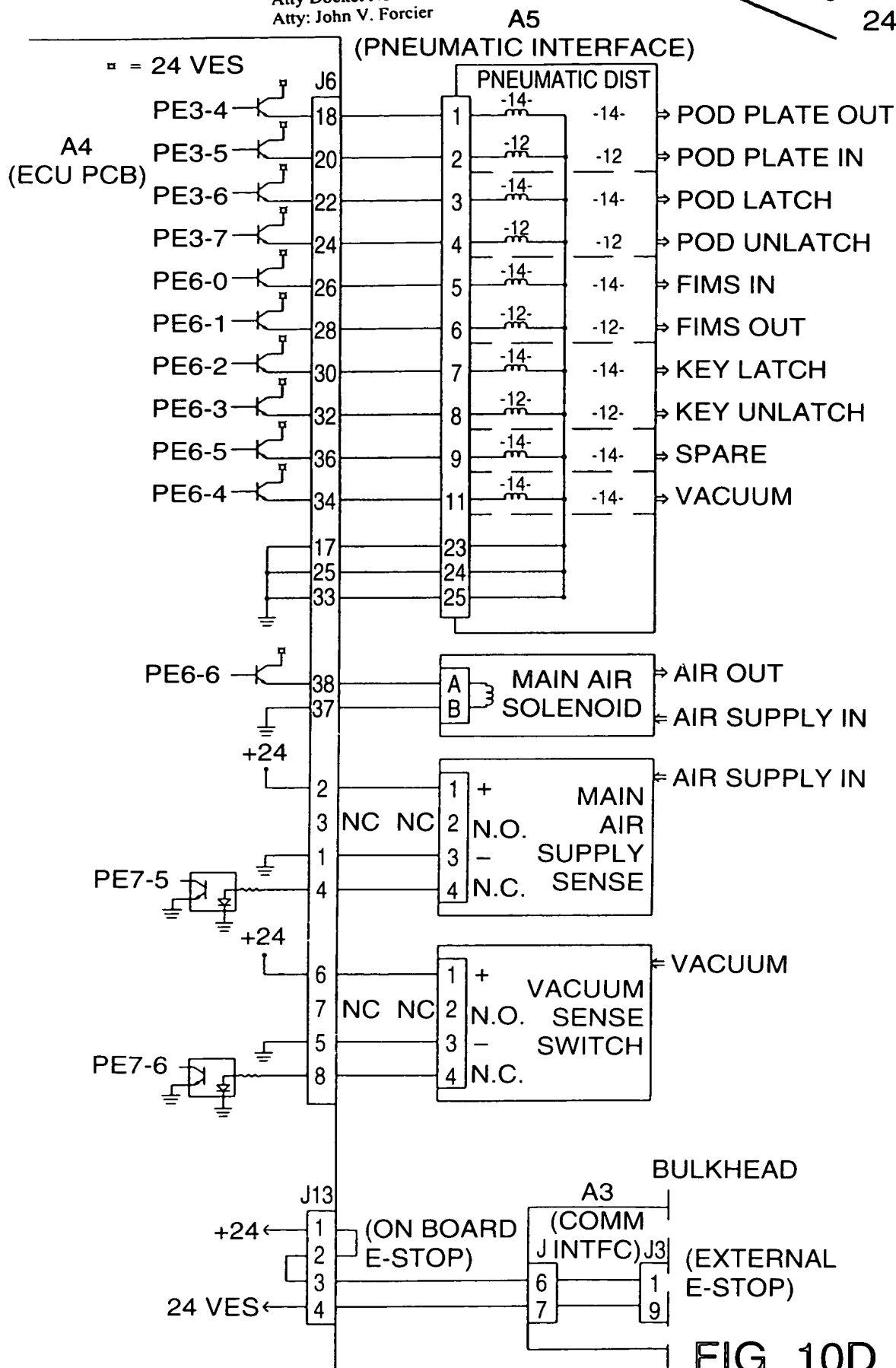
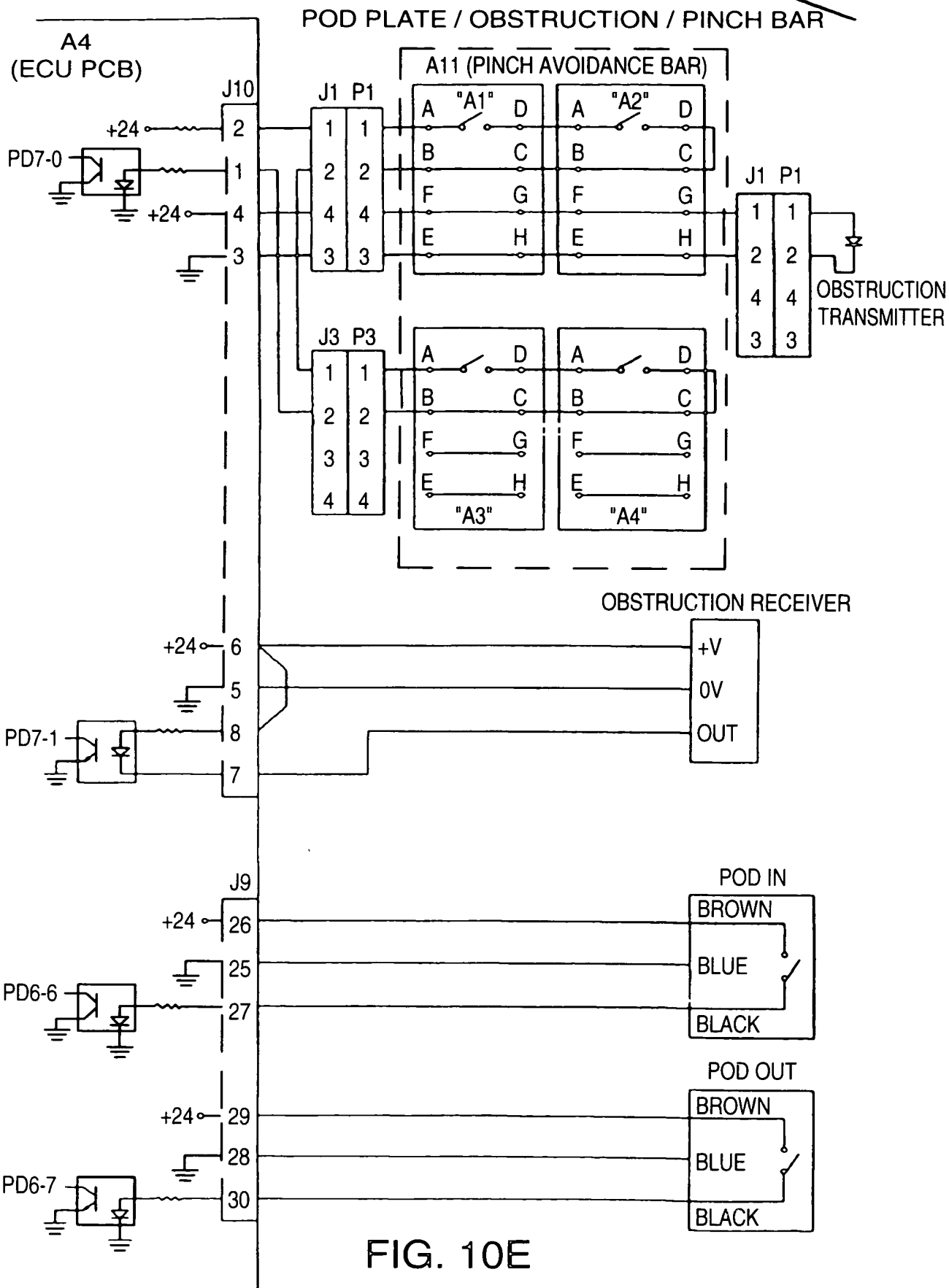
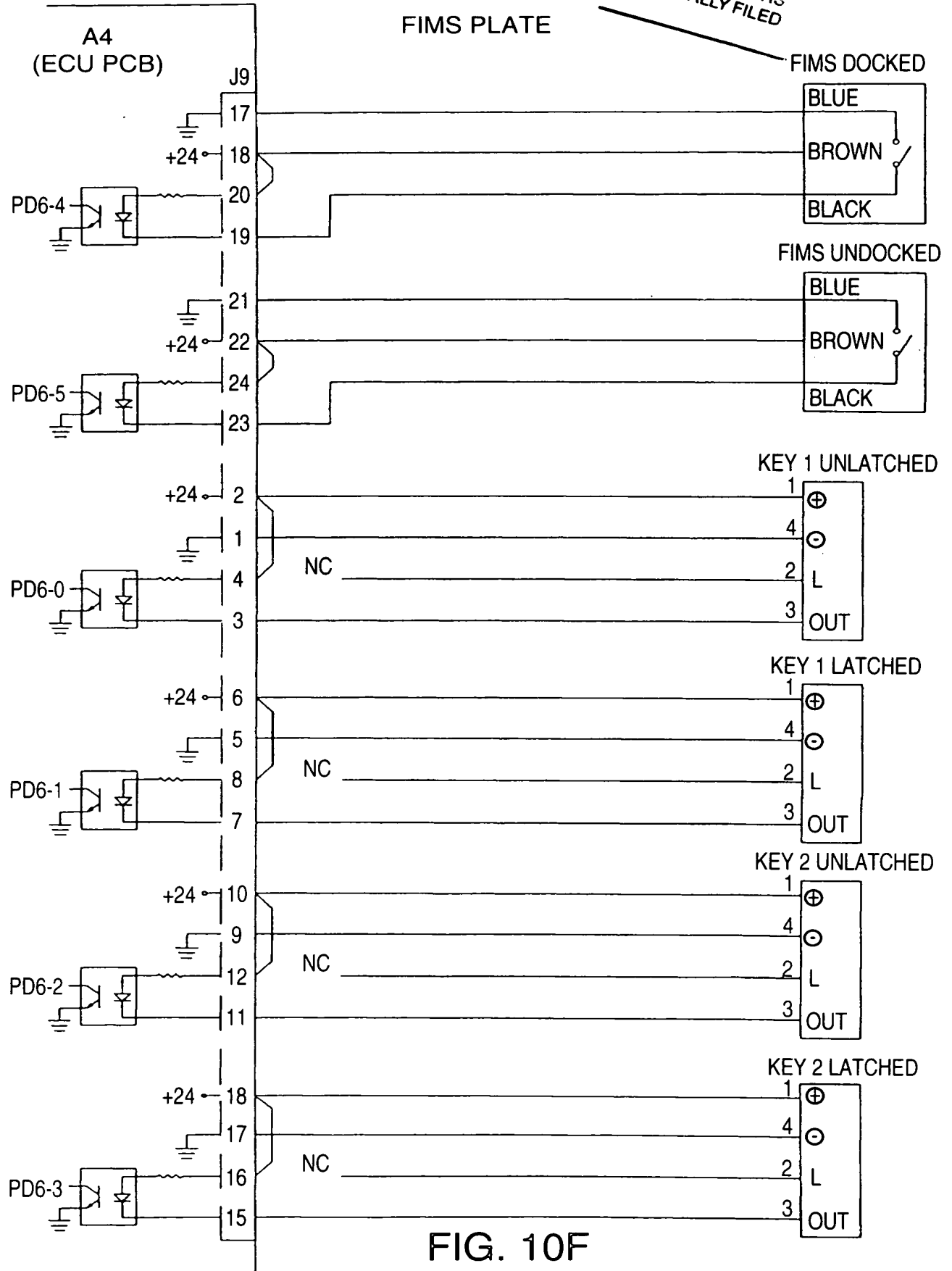


FIG. 10D



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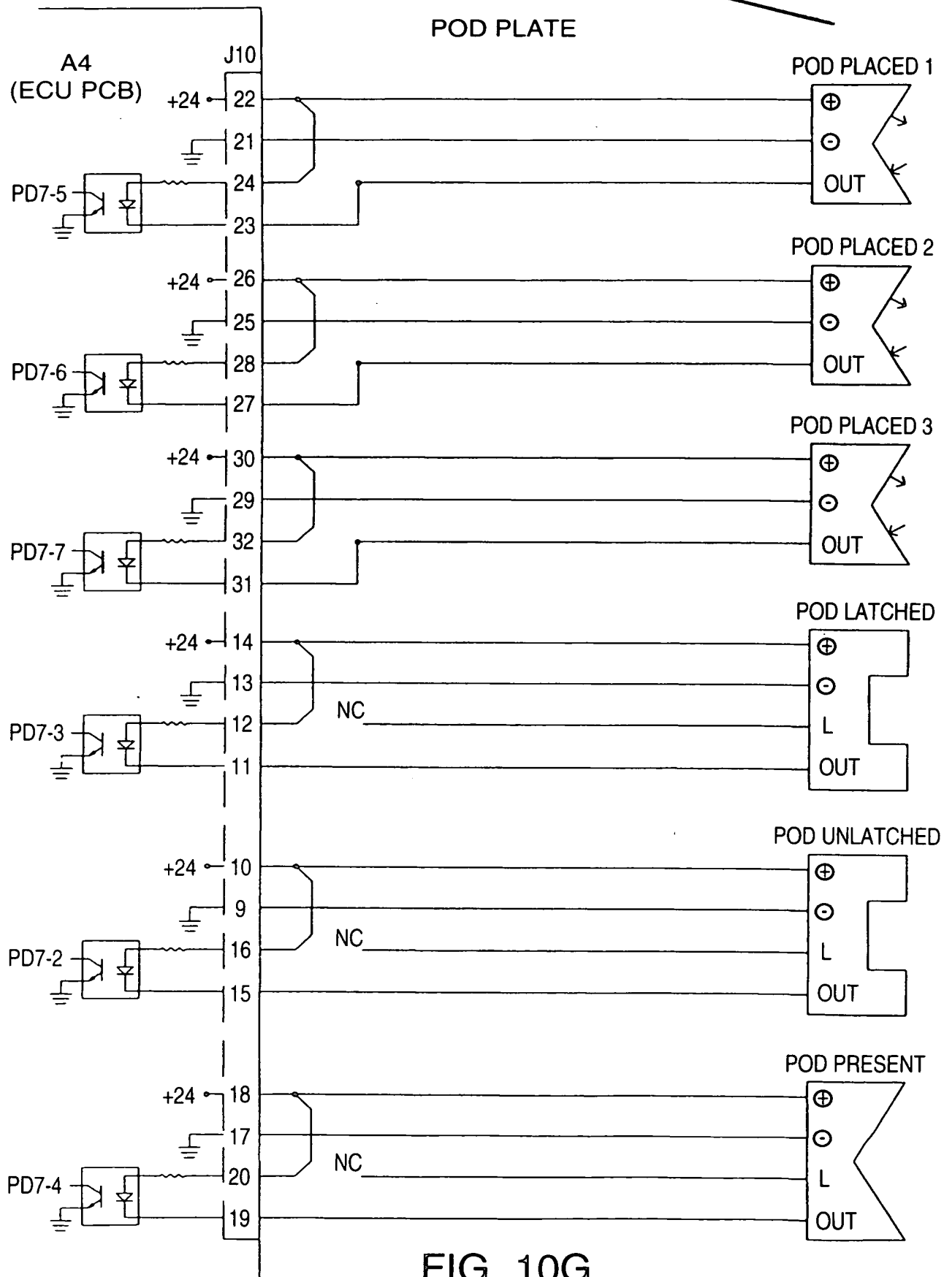


FIG. 10G

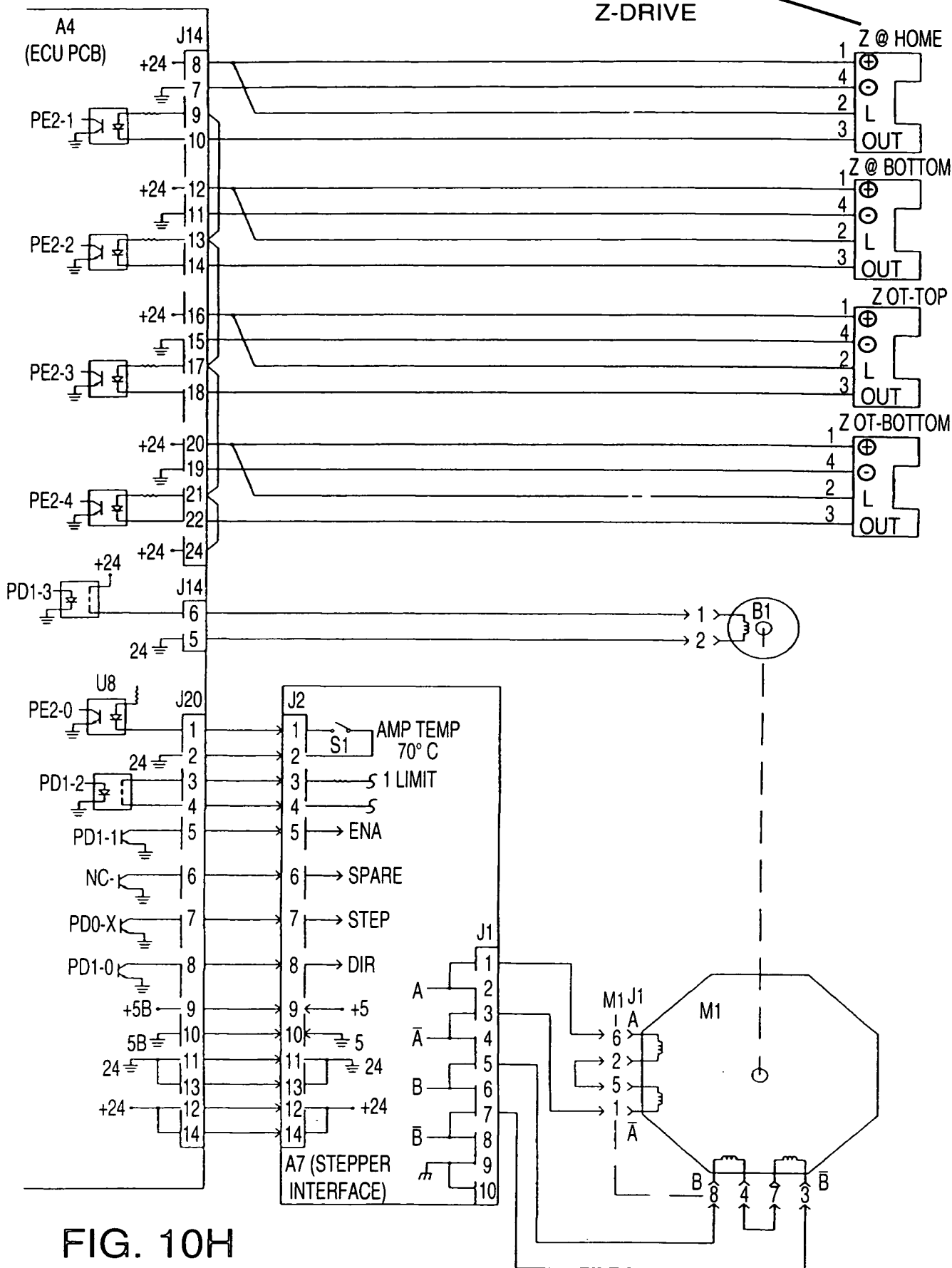


FIG. 10H